



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

H. ADACHI et al

Serial No. 10/006,243

Group Art Unit: 2825

Filed: December 10, 2001

Examiner: P. Kik

For: MANUFACTURING METHOD OF SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

RECEIVED

PETITION FOR EXTENSION OF TIME

AUG 1 4 2003

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Technology Center 2100

Sir:

It is respectfully requested that a one-month Extension of Time, to and including August 11, 2003, be granted in which to respond to the Office Action dated April 10, 2003 in the above-identified application.

Our Credit Card Payment Form in the amount of \$110.00 is attached in payment of the appropriate fee.

The Commissioner is hereby authorized to charge any additional payment due, or to credit any overpayment, to Deposit Account No. 50-1417.

08/13/2003 RMEBRAHT 00000100 09606050

01 FC:1251

110.00 OP

Respectfully submitted,

parker J. Stanger

Registration No. 32,846
Attorney for Applicant(s)

MATTINGLY, STANGER & MALUR, P.C. 1800 Diagonal Road, Suite 370 Alexandria, Virginia 22314 Telephone: (703) 684-1120

Date: August 11, 2003